### **Exploiting Software Vulnerabilities**

## Software Vulnerabilities Control-Flow Hijacking

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Dept. of Computer Science and Systems Engineering University of Zaragoza, Spain

Course 2022/2023

#### Master's Degree in Informatics Engineering

University of Zaragoza Seminar A.22, Ada Byron building



#### Outline

- A little recap
- 2 Buffer Overflows
- 3 Defenses against Control-Flow Hijacking Attacks
  - Stack Data Protection
  - Non-Executable Stack
  - Write XOR eXecute (W<sup>^</sup>X) Pages
  - Address Space Layout
  - Other Techniques of Defense



#### Outline

- A little recap
- Defenses against Control-Flow Hijacking Attacks

## Recap on...

#### **Definitions**

#### System/defender perspective

- Attack surface
  - Exposure of a system to attacks
- Vulnerability
  - Software flaw that can be exploited by an attacker

#### Attacker perspective

- Attack vector
  - How the attack was carried out
- Exploit
  - Succeed by taking advantage of a vulnerability

### Recap on...

#### **Vulnerabilities**

#### Types of software vulnerabilities

- Overflow
  - Buffer overflow
  - Heap overflow
- NULL pointer dereference
- Dynamic memory handling
  - Use-after-free
  - Double free
  - Allocator abuse

- Number handling
- Format strings
- Uninitialized memory
- Race conditions

#### **Vulnerability databases**

- National Vulnerability Database (NVD), maintained by NIST (https://nvd.nist.gov/)
- MITRE CVE (https://cve.mitre.org/)
- Bugtraq (http://www.securityfocus.com/archive/1)
- . . . .



### Today we talk about...

### **Control-Flow Hijacking**

- Attacker's goal: to seize the target system
  - Run arbitrary code to hijack the control flow of a vulnerable application

- Block of instructions that performs a specific task
- Three components:
  - Input (values passed from the caller)
  - Body (code to perform the task)
  - Return value (to the caller)
- Calling a function involves a branch in the control flow (i.e., jumping to another location)
  - The return address is usually stored in the caller's stack frame

```
int x = compute(arg0, arg1, ...)
```

What happens in the backstage before a function runs?

- Parameters are configured to be passed to the function
  - Either through the stack or logical registers
- The address of the next instruction after the call is also saved

```
int x = compute(arg0, arg1, ...)
```

What happens in the backstage before a function runs?

- Parameters are configured to be passed to the function
  - Either through the stack or logical registers
- The address of the next instruction after the call is also saved

What happens in the backstage after a function runs?

- Return value is set
  - Normally, the logical register eax contains the return value of a function
  - Allocated variables (within the function) are removed from the stack
  - Registers used in the function are restored to their previous values
  - The control is transferred to the saved return address.

#### Standard prologue

- Occurs at the beginning of a function
- Allocates space for local variables (on stack)
- Saves registers to be reused in the body of the function

#### ■ Standard epilogue

- Occurs at the end of a function
- Normally, undoes what was done in the prologue
- Cleans up the stack
- Restores register values

mov edi, edi

#### **Common prologue on Windows**

- 2-byte length instruction
- Equivalent for a nop instruction, since it does *nothing*
- Used to hot-patch a running executable, without stopping and restarting it
  - Can be overwritten with a relative jump of 2 bytes!

Further reading: Why do Windows functions all begin with a pointless MOV EDI, EDI instruction?, R. Chen, 2011.

https://devblogs.microsoft.com/oldnewthing/?p=9583

#### Calling conventions

- Describes how data is passed in/out of functions
- Implementation may vary by compiler

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- Describes how data is passed in/out of functions
- Implementation may vary by compiler

#### cdec1 convention (most common)

- Arguments are pushed onto the stack from right to left
- Return value is placed in eax
- The caller must clean the stack (removing passed parameters)

#### stdcall convention

- Similar to cdec1, but *callee* clears the stack
- Convention used in Windows APIs

#### Standard calling conventions

#### stdcall convention

- Similar to cdec1, but *callee* clears the stack
- Convention used in Windows APIs

#### fastcall convention

- Arguments are passed by registers, put on the stack when a large number of arguments are required
  - For instance, the GCC and Microsoft compilers use the ecx and edx registers
- The callee clears the stack
- Return value is placed in eax

#### thiscall convention

- Used in C++ in object methods (member functions)
- Includes a reference to the this pointer
- Depends on the compiler:
  - In Microsoft compilers, ecx holds the this pointer and the callee clears the stack
  - In GNU compilers, the this pointer is pushed last and the *caller* clears the stack

```
#include <iostream>
using namespace std;
class Student {
    public:
        int id: //data member (also instance variable)
        string name; //data member (also instance variable)
    void imprime(){
        cout << this -> id << endl;
        cout << this -> name << endl;
};
_cdecl int echo(int x){
    return x + 8;
int main() {
    Student s1; //creating an object of Student
    s1.id = 201;
    s1.name = "Sonoo Jaiswal";
    s1.imprime();
    printf("echo: %d\n", echo(4));
    return 0;
```

```
PUSH EBP
MOV EBP.ESP
                     55
89E5
00401426
00401428
                     53
                                         PUSH EBX
00401429
                     51
                                         PUSH ECX
                     83EC 30
                                         SUB ESP.0x30
0040142A
00401420
                     E8 3E060000
                                          CALL a.00401A70
00401432
                     8D45 DC
                                         LEA EAX.DWORD PTR SS:[EBP-0x24]
                     89C1
0040143
                                         MOV ECX.EAX
00401437
                     E8 10290000
                     C745 DC C900 MOV DWORD PTR SS:[EBP-0x24],0xC9
8D45 DC LEA EAX,DWORD PTR SS:[EBP-0x24]
0040143
00401443
00401446
                     83C0 04
                                         ADD EAX,0x4
ññ4ñ1449
                     C70424 45504 MOV DWORD PTR SS:[ESP].a.00405045
                                                                                                            ASCII "Sonoo Jaiswal"
00401450
                     89C1
                                         MOV ECX.EAX
00401452
                     E8 E1000000
                                         CALL CALL 
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                     83EC 04
00401457
                                         SUB ESP.0x4
0040145A
                     8D45 DC
                                         LEA EAX.DWORD PTR SS:[EBP-0x24]
0040145D
                     89C1
                                         MOV ECX, EAX
0040145F
                     E8 8C280000
                                         CALL a.00403CF0
00401464
                     C70424 04000 MOV DWORD PTR SS:[ESP].0x4
0040146B
                     E8 AØFFFFFF
                                         CALL a.00401410
00401470
                     894424 04
                                         MOV DWORD PTR SS:[ESP+0x4],EAX
                     C70424 53504 MOV DWORD PTR SS:[ESP].a.00405053
                                                                                                           ASCII "echo: %d⊡"
00401474
0040147B
                     E8 50270000
                                                         .&msvert.printf)
00401480
                     BB 00000000
                                         MOV EBX.0x0
00401485
                     8D45 DC
                                         LEA EAX, DWORD PTR SS: [EBP-0x24]
00401488
                     8901
                                         MOV ECX, EAX
0040148A
                     E8 D9280000
                                          CALL a.00403D68
0040148F
                     89D8
                                         MOV EAX.EBX
00401491
                    EB 16
                                                         a.004014A9
00401493
00401495
                                          MOV EBX.EAX
                     89C3
8D45 DC
                                         LEA EAX.DWORD PTR SS:[EBP-0x24]
00401498
                     8901
                                         MOV ECX, EAX
004014<u>9</u>A
                     E8_C9280000
                                          CALL a.00403D68
                                         MOV EAX,EBX
0040149F
                     89D8
004014A1
                     890424
                                         MOV DWORD PTR SS:[ESP], EAX
                     E8 670C0000
                                                  KJMP.&libaco s dw2-1. Unwind Resum
004014A4
                                         LEA ESP,DWORD PTR SS:[EBP-0×8]
POP ECX
004014A9
004014AC
                     8D65 F8
                     59
58
004014AD
                                         POP EBX
004014AE
                                          POP EBP
                     8D61 FC
                                          LEA ESP,DWORD PTR DS:[ECX-0x4]
004014AF
                                                                                                                                               iversidad
004014B2
                     ĊЗ
```

```
00401331
                      89E5
                                            MOV EBP,ESP
PUSH ESI
00401333
                     56
00401334
                                            PŬŠH EBX
                      83EC 10
                                            SUB ESP,0x10
00401
004013
                     C70424 00504
                                            MOV DWORD PTR SS:[ESP],a.00405000
                                                                                                                ASCII_"libgcc_s_dw2-1.dll"
                      E8 4C290000
83EC 04
004013
                                                                         32.GetModuleHandleA>
00401344
                                            SUB ESP,0x4
00401347
                                            TEST EAX, EAX
                     85C0
00401349
                      74 75
0040134B
                      C70424 00504 MOV DWORD PTR SS:[ESP].a.00405000
                                                                                                                 ASCII "libacc s dw2-1.dll"
00401352
                      8903
                                            MOV EBX.EAX
00401
                      E8 17290000
                                            CALL CALL 
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                                           SUB ESP,0x4
MOV DWORD PTR DS:[0x407070],EAX
00401359
                      83EC 04
                     A3 70704000
00401
                                           MOV DWORD PTR SS:[ESP+0x4],a.00405013
MOV DWORD PTR SS:[ESP],EBX
00401
                      C74424 04 13
                                                                                                                 ASCII "__register_frame_info"
00401369
                      891C24
                      E8 17290000
004013
                                                               KERNEL32.GetProcAddress>
00401371
                      83EC 08
                                           SUB ESP,0x8
MOV ESI,EAX
00401374
                                                                                                                  ASCII "__deregister_frame_info"
00401376
                     C74424 04 29!
891C24
                                           MOV DWORD PTR SS:[ESP+0x4],a.00405029
0040137E
                                            MOV DWORD PTR SS:[ESP].EBX
00401381
                     E8 02290000
                                                                                       ocAddress>
00401
                     A3 00404000
                                           MOU DWORD PTR DS:[0x404000].EAX
                      83EC 08
00401
                                            SUB ESP,0x8
                                            TEST ESI.ESI
00401
                      85F6
00401
                      74 11
00401392
                      C74424 04 08 MOV DWORD PTR SS:[ESP+0x4],a.00407008
0040139A
004013A1
                      C70424 B8604 MOV DWORD PTR SS:[ESP],a.004060B8
004013A3
                      C70424 E0134 MOV DWORD PTR SS:[ESP],a.004013E0
004013AA
                                            CALL (JMP.&msvcrt.atexit)
                                            LEA ESP, DWORD PTR SS: [EBP-0x8]
004013AF
                      8D65 F8
004013B2
                                           POP EBX
POP ESI
004013B3
004013B4
                                            POP EBP
                                            RETN
```

#### Pushing data on the stack: mov vs. push

- push always subtracts 4 from the esp register
- mov puts a value on the stack, but does not subtract from esp
- Optimization issue: small performance gain at runtime
  - When used with the stdcall convention, the caller must make special settings

#### Pushing data on the stack: mov vs. push

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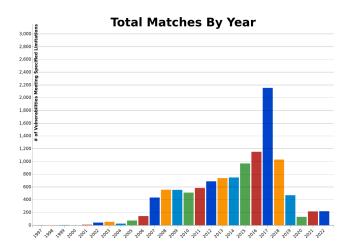
#### **Inline functions**

- Eliminate costly control transfers
- Useful for small functions, as their body is inlined with the caller's body
- No extra overhead for entry/exit
- Occurs often with string-related functions

#### Outline

- A little recap
- **Buffer Overflows**
- Defenses against Control-Flow Hijacking Attacks

#### Most common vulnerability in C/C++ programs



Credits: taken at 27/10/2022, https://nvd.nist.gov/



#### A bit of history – the first BOF exploited

- (BSD-derived) UNIX fingerd daemon
  - Utility that allows users to obtain information about other users
  - Usually used to identify the full name or login name of a user, whether a user is currently logged in or not, and other user information
- Morris worm (November 2 1988!)
  - Affected Sun 3 systems and VAX computers running 4 BSD UNIX variants
  - Exploited a buffer overflow in fingerd to create a remote shell

```
8) The infection attempts proceeded by one of three routes: rsh, fingerd, or sendmail
     8a) The attack via rsh was done by attempting to snawn a remote shell by invocation of
          (in order of trial) /usr/uch/rsh, /usr/bin/rsh, and /bin/rsh. If successful, the host was
          infected as in steps 1 and 2a, above
    8b) The attack via the fineer daemon was somewhat more subtle. A connection was
         established to the remote finger server daemon and then a specially constructed
          string of 536 bytes was passed to the daemon, overflowing its input buffer and
         overwriting parts of the stack. For standard 4 BSD versions running on VAX com-
          puters, the overflow resulted in the return stack frame for the main routine being
         changed so that the return address pointed into the buffer on the stack. The instruc-
         tions that were written into the stack at that location were:
         push1
                    $687324
                    $6e69622f
                                      '/bin'
                     sp, r10
                    80
                   $0
         pushl
                   r10
         pushl
                    $3
         mourl
                     sp, ap
         chmk
                     $3b
          That is, the code executed when the main routine attempted to return was
                                       execve("/bin/sh", 0, 0)
         On VAXen, this resulted in the worm connected to a remote shell via the TCP con-
         nection. The worm then proceeded to infect the host as in steps 1 and 2a, above
         On Suns, this simply resulted in a core file since the code was not in place to corrunt
         a Sun version of fingerd in a similar fashion.
    8c) The worm then tried to infect the remote host by establishing a connection to the
```

Further reading: The internet worm program: an analysis. E.H. Spafford. 1989. SIGCOMM Comput. Commun. Rev. 19, 1, 17457. doi:dad

SMTP port and mailing an infection, as in step 2b, above.

## Buffer overflows What we need to know

- How the stack works
- Calling conventions
- How system calls are made

### Anything else?...

- Target system CPU
  - Little-endian vs. big-endian
- Target system operating system
  - UNIX vs. Windows: stack frame changes!

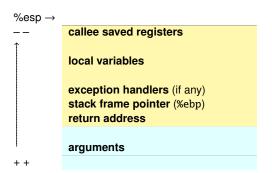
#### Linux x86 process memory layout

0xFFFFFFF	kernel space
	(1GiB)
0xC0000000	user stack
<u></u>	
0x40000000	shared libraries
<u> </u>	runtime heap
	bss
$\uparrow$	static data
0x08048000	(ELF binary loaded here)
0	unused

Check output of: cat /proc/<PROCESS PID>/maps



#### Stack frame



- Stack Pointer (%esp): top of the stack
- Base Pointer (%ebp): base of the current frame
- Function arguments belong to the previous stack frame
  - Each function defines its own stack frame

Note: Stack grows to lower memory addresses



## **Buffer overflows** Stack concepts summary

- The stack stores abstract data
- Last-In-First-Out (LIFO) policy
- Assembly instructions of interest:
  - push: inserts an item on top of the stack, and decreases %esp by 4 bytes (dword size)
  - pop: eliminates the item at the top of the stack, and increments %esp by 4 bytes

## **Buffer overflows** Stack concepts summary

- The stack stores abstract data
- Last-In-First-Out (LIFO) policy
- Assembly instructions of interest:
  - push: inserts an item on top of the stack, and decreases %esp by 4 bytes (dword size)
  - pop: eliminates the item at the top of the stack, and increments %esp by 4 bytes
  - call: inserts as the address of the next instruction which immediately follows the call on top of the stack, and decreases %esp by 4 bytes
  - Return of functions. %esp is incremented after execution. They accept an optional immediate value, which increments more %esp
    - retn: near return, retrieves the top of the stack and sets it as %eip
    - retf: far return, retrieves two dwords from the top of the stack and sets them as %eip and cs (code segment), respectively. Note that although cs is word size, it takes two dwords off from stack!



#### Stack concepts summary On 32-bit architectures

- Function arguments
- Return address
- Local variables

#### On 64-bit architectures

- Also stores function arguments, but differs from 32-bit architectures:
  - UNIX uses System V Application Binary Interface (ABI): first 6 integer (or pointer) arguments to a function are passed in registers (%rdi, %rsi, %rdx, %rcx, %r8, and %r9); from the 7th argument onwards, the stack is used
  - Microsoft ABI: only 4 registers are used (%rcx, %rdx, %r8, and %r9); from the 5th argument onwards, the stack is used
- Return address
- Local variables



### Example

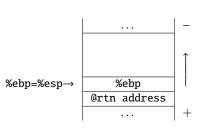
```
void readName(){
   char username[256];
   printf("Type user name: ");
                                             %eip:
                                                     push ebp
   scanf("%s", username);
}
readName:
        push ebp
       mov
              ebp. esp
       sub
              esp, 264
       sub
              esp, 12
       push
              OFFSET FLAT:.LC0
       call
              printf
       add
              esp, 16
                                                       @rtn address
                                             %esp→
       sub
              esp. 8
       lea
              eax. [ebp-264]
                                                                        +
       push
              eax
       push
              OFFSET FLAT: .LC1
       call.
              isoc99 scanf
       add
              esp, 16
       leave
       ret
```

# Buffer overflows Example

```
void readName(){
   char username[256];
   printf("Type user name: ");
                                            %eip: mov ebp, esp
   scanf("%s", username);
}
readName:
       push
              ebp
        mov ebp. esp
       sub
              esp, 264
       sub
             esp, 12
       push
             OFFSET FLAT:.LC0
                                                           %ebp
       call
                                             %esp→
             printf
       add
             esp, 16
                                                      @rtn address
       sub
             esp. 8
       lea
             eax. [ebp-264]
                                                                       +
      push
              eax
       push
             OFFSET FLAT: .LC1
       call.
             isoc99 scanf
       add
             esp, 16
       leave
       ret
```

# Buffer overflows Example

```
void readName(){
   char username[256];
   printf("Type user name: ");
   scanf("%s", username);
}
readName:
       push
              ebp
              ebp, esp
       mov
        sub esp, 264
       sub
              esp, 12
       push
              OFFSET FLAT:.LC0
       call
              printf
       add
              esp, 16
       sub
              esp. 8
       lea
              eax. [ebp-264]
       push
              eax
       push
              OFFSET FLAT: .LC1
       call.
              isoc99 scanf
       add
              esp, 16
       leave
       ret
```



%eip: sub esp, 264

## Buffer overflows Example

```
void readName(){
                                             %eip: sub esp, 264 (after)
   char username[256];
   printf("Type user name: ");
   scanf("%s", username);
                                                                         _
                                                             . . .
}
                                                                         (offset 264)
                                             %esp→
readName:
       push
              ebp
              ebp, esp
       mov
        sub esp, 264
       sub
              esp, 12
       push
              OFFSET FLAT:.LC0
       call
              printf
       add
              esp, 16
       sub
              esp. 8
       lea
              eax. [ebp-264]
                                             %ebp→
                                                            %ebp
       push
              eax
       push
              OFFSET FLAT: .LC1
                                                       @rtn address
       call.
              isoc99 scanf
       add
              esp, 16
                                                                         +
       leave
       ret
```

#### Example

```
%eip: lea eax, [ebp-264]
void readName(){
   char username[256];
   printf("Type user name: ");
   scanf("%s", username);
                                             %esp→
}
                                                        @username
                                                                        ←%ebp - 264
readName:
       push
              ebp
              ebp, esp
       mov
              esp, 264
       sub
       sub
              esp, 12
              OFFSET FLAT: . I.CO
       push
       call.
              printf
       add
              esp, 16
       sub
              esp. 8
        lea eax, [ebp-264]
       push
              eax
                                                            %ebp
                                             %ebp→
       push
              OFFSET FLAT: .LC1
       call.
              isoc99 scanf
                                                       @rtn address
       add
              esp, 16
                                                                        +
       leave
       ret
                                                                               Universidad
```

#### Example

■ What if username is more than 264 bytes long?

#### Example

```
%esp→

%esp→

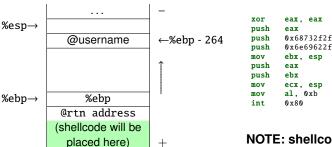
@username

char username[256];
printf("Type user name: ");
scanf("%s", username);
}

%ebp→
%ebp
%ebp
%ebp
%char username
... +
```

- What if username is more than 264 bytes long?
  - The adjacent memory to username is overwritten, since scanf does not check for any buffer limits (it is an insecure function)
  - Arbitrary code execution, since %eip will pop the top value of the stack when the function returns!

#### Basic stack exploit



# NOTE: shellcode runs on the stack

#### Insert your shellcode on the stack

■ Shellcode: originally, the minimal code to launch a shell (i.e., exec("/bin/sh")). Today, any code injected regardless of its purpose

#### 2 Manipulate @rtn address to return to your shellcode

- Look for assembly instructions that allow redirection of execution to %esp
- When the vulnerable function ends, the shellcode runs!

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Further reading: Smashing The Stack For Fun And Profit. Aleph One, Phrack 49 (1996), http://phrack.org/issues/49/149html

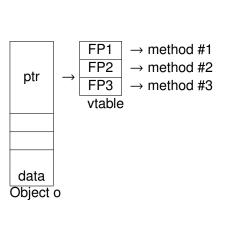
Insecure libc functions – (non-exhaustive list)

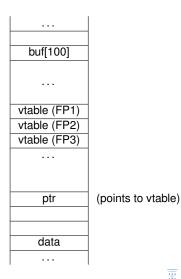
- strcpy → strncpy → strlcpy/strcpy\_s (Windows CRT)
- strcat → strncat → strlcat/strcat\_s (Windows CRT)
- strtok
- $\blacksquare$  sprintf  $\rightarrow$  snprintf
- $vsprintf \rightarrow vsnprintf$
- $\blacksquare$  qets  $\rightarrow$  fqets/qets\_s
- scanf/sscanf → sscanf\_s (Windows CRT)
- snscanf → \_snscanf\_s (Windows CRT)
- strlen → strnlen\_s (Windows CRT)

#### Some safe versions are misleading

strncpy, strncat can leave strings unfinished – be careful!

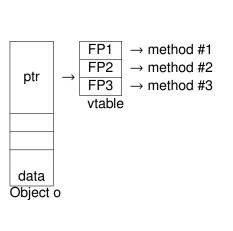
## Corrupting method pointers – Heap overflow

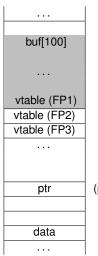




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## Corrupting method pointers – Heap overflow





(points to vtable)

How to hunt overflows...

#### Find the overflow

- Configure the operating system correctly (core dump?)
- Issue malformed inputs with specific endings
  - Automated tools (fuzzers)
- If the application crashes, check the CPU registers for the endings

How to hunt overflows...

#### Find the overflow

- Configure the operating system correctly (core dump?)
- Issue malformed inputs with specific endings
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- If the application crashes, check the CPU registers for the endings

#### **Build the exploit**

- Analyze overflow conditions
- Check if the overflow can lead to arbitrary code execution
  - Not easy, given the latest built-in defenses at the OS level

## Outline

- 1 A little recap
- 2 Buffer Overflows
- 3 Defenses against Control-Flow Hijacking Attacks
  - Stack Data Protection
  - Non-Executable Stack
  - Write XOR eXecute (W<sup>X</sup>) Pages
  - Address Space Layout
  - Other Techniques of Defense

# Defeating control-flow hijacking attacks Approaches

- 1 Fix bugs:
  - Audit software to find bugs (there are automated tools soundness?)
  - Re-code software in a type-safe language
- 2 Allow overflow, but prevent injected code from running
- 3 Insert runtime code to detect overflows
  - Process stops when overflow is detected

Further readings: SoK: Eternal War in Memory. L. Szekeres, M. Payer, T. Wei and D. Song. 2013 IEEE Symposium on Security and

Privacy, Berkeley, CA, 2013, pp. 48-62, doi: 10.1109/SP.2013.13

Memory Errors: The Past, the Present, and the Future, V. van der Veen, N. dutt-Sharma, L. Cavallaro, H. Bos (2012), In Research in

Attacks, Intrusions, and Defenses, RAID 2012, LNCS, vol 7462, Springer, doi: 10.1007/978-3-642-33338-5 5



# Defeating control-flow hijacking attacks Stack data protection

### Stack cookies

- Detect stack-based overflows by:
  - In the function prologue, push a magic number
    - In the function epilogue, check this value

# Defeating Control-Flow Hijacking Attacks Stack cookies

- Initial ideas come from StackGuard (Crispin Cowan, 1997)
- **Enhanced by Hiroaki Etoh with ProPolice (2000)** 
  - Later renamed to SSP (Stack-Smashing Protector), included in mainstream GCC version 4.1
- Types of canaries:
  - Null canary (all zeros; 0x00000000)
  - Terminator canary (0x000aff0d)
    - 0x00 stops strcpy() (and related functions)
    - 0x0a and 0x0d stop gets() (and related functions)
    - 0xff (EOF) stops other functions
  - Random canary



# Defeating control-flow hijacking attacks Stack cookies

#### How to protect information stored after the vulnerable buffer?

- Add a canary after each buffer and check each time before accessing any other data stored after it
  - Good idea, may be a compiler modification
  - However, not practical: performance impact
- Reorder local variables on the stack to move the sensitive data out of the way of the buffer overflow
  - Side effect of compiler optimizations
  - Implemented as an intentional protection in ProPolice: ideal stack layout
    - Places local buffers at the end of the stack frame
    - Relocates other local variables before them.
  - Also introduced by Microsoft Visual Studio (/GS feature)

# Defeating control-flow hijacking attacks Stack cookies

### Ideal stack layout does not always exist...

- Multiple local buffers are placed one after another
- Structure members cannot be rearranged (interoperability issues)
- Particular structures (like arrays of pointers) can be overflowed or be treated as sensitive information, depends on the semantics
- Functions with a variable number of arguments remain unprotected
- Dynamically created buffers on the stack (e.g., alloca()) are placed at the top of the stack frame

# Defeating control-flow hijacking attacks Stack cookies

```
readName:
                                                                          ebp
                                                                 push
                                                                          ebp, esp
                                                                 mov
                                                                 sub
                                                                          esp, 280
readName:
                                                                 mov
                                                                          eax. DWORD PTR qs:20
        push
                 ebp
                                                                          DWORD PTR [ebp-12], eax
                                                                 mov
                 ebp. esp
        mov
                                                                 xor
                                                                          eax, eax
        suh
                 esp. 264
                                                                          esp. 12
                                                                 suh
        sub
                 esp, 12
                                                                 push
                                                                          OFFSET FLAT: ICO
        push
                 OFFSET FLAT: LCO
                                                                 call.
                                                                          printf
        call.
                 printf
                                                                 add
                                                                          esp. 16
        add
                 esp, 16
                                                                 suh
                                                                          esp. 8
        sub
                 esp, 8
                                                                 lea
                                                                          eax, [ebp-268]
        1ea
                 eax. [ebp-264]
                                                                 push
                                                                          eax
        push
                 eax
                                                                 push
                                                                          OFFSET FLAT: LC1
                 OFFSET FLAT: . I.C1
        push
                                                                 call.
                                                                          isoc99 scanf
        call.
                 isoc99 scanf
                                                                 add
                                                                          esp, 16
        add
                 esp, 16
                                                                          eax. DWORD PTR [ebp-12]
                                                                 mov
        leave
                                                                 xor
                                                                          eax. DWORD PTR qs:20
        ret
                                                                          .1.2
                                                                 je
                                                                 call.
                                                                          stack chk fail
(stack cookies disabled)
                                                         .L2:
                                                                 leave
```

ret

(stack cookies enabled)

#### Bypassing it is still possible

■ On Windows, SEH-based exploits

■ On UNIX-like systems, we need a memory leak (or bruteforce)

## **BOF** exploitation steps

- 1 Place code in the stack (in the same vulnerable buffer)
- 2 Overwrite a return address
- 3 Jump to it

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#### Non-executable stack

- First implemented for DEC on Alpha in Feb 1999
- Enabled by default on most desktop platforms, such as Linux, macOS, and Windows
- Main weaknesses
  - Still allows the return address to be abused, overwriting it with an arbitrary location
  - Does not prevent the execution of code already present in the process memory or code injected in other data areas

# Defeating control-flow hijacking attacks Non-executable stack

## Bypassing techniques

- return-into-libc (ret2libc for short)
  - Use libc function addresses as return addresses.
  - The attacker does not require any shellcode to take control of a target, they simply redirect the execution of the control flow as they wish
  - We will talk about this more in deep in the last part of the course!

#### Improved techniques:

- ret2plt
- ret2syscall
- ret2strcpy, ret2gets (or read(), recv(), recvfrom() variants)
- ret2data
- ret2text. ret2code. ret2dl-resolve
- Chained ret2code (or chained ret2libc)

# W<sup>^</sup>X (memory) pages

- Logical extension of non-executable stacks
- Non-executable writable pages and non-writable executable pages

## W<sup>^</sup>X (memory) pages

- Logical extension of non-executable stacks
- Non-executable writable pages and non-writable executable pages
- Term coined by Theo de Raadt (founder and main architect of OpenBSD)
- First implementation of W<sup>A</sup>X: 1972! (Multics on the GE-645 mainframe)

# Defeating control-flow hijacking attacks Write XOR eXecute (W<sup>X</sup>) pages

- The PaX project (Oct 2000)
  - Linux kernel patch for Intel x86 hardware
  - Today, it is available for almost all hardware platforms
  - It was never included in mainstream Linux distribution, although today most distributions have some kind of W^X
- On-chip support for non-executable pages came a bit later
  - NX: Non-eXecutable feature (AMD Athlon 64: Sept 2003)
  - ED: Execute-Disable feature (Intel P4 Prescott; Feb 2004)
  - XN: eXecute-Never feature (ARM v6)
- Software that took advantage of hardware support emerged a few months later
  - Linux kernel patches (via PaX project)
  - Microsoft Windows XP Service Pack 2 (Data Execution Prevention; DEP opt-in by default)



# Defeating control-flow hijacking attacks Can we still execute arbitrary injected code when W<sup>\X</sup> is on?

- Do we really need to inject new code? Otherwise, ret2code
- Is there a page with W+X permissions? If so, ret2strcpy or ret2gets
- Can we chain the existing code, using ret2code, to write an executable file to disk and then run it?

# Defeating control-flow hijacking attacks Can we still execute arbitrary injected code when W<sup>\X</sup> is on?

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- Can we chain the existing code, using ret2code, to write an executable file to disk and then run it?
- Is there a way to turn the protection off?
  - SetProcessDEPPolicy / ZwSetInformationProcess on Windows platforms
- $\blacksquare$  Can we change the permissions of a specific memory region from  $W^{\wedge}X$  to  $W_{\pm}X$ ?
  - VirtualProtect on Windows platforms
  - mprotect on GNU/Linux platforms
    - note: PaX does not allow a page to be W+X, nor X after W
    - In kernel, it requires the memory address to be aligned to 4KiB
- Can we create a new memory region with W+X permissions?
  - VirtualAlloc() on Windows platforms
  - mmap() on Unix-like platforms
    - As before, not allowed if PaX is installed.
  - You will first need to copy the injected code and then jump there (chained ret2code: mmap-strcpy-code)



- ret2libc allows us to bypass non-executable stacks
  - Addresses of functions are known and are part of the attacker's input

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## **ASCII Armored Address Space (AAAS)**

- Linux kernel patch that loaded all shared libraries into memory addresses starting with a null byte
- Similar idea to terminator canaries
- Protects against strcpy-like exploitation, but not gets
- Still vulnerable to other ret2- attacks

# Address Space Layout Randomization (ASLR)

- Randomizes the address of everything (libraries, image, stack, and heap)
- Prevents the attacker from knowing where to jump or where to point pointers
- First implemented in PaX for Linux in 2001:
  - "unless every address is randomized and unpredictable, there's always going to be room for some kind of attack"
- Introduced in Windows Vista (2007)
- NOTE: if the attacker can inject code and there is enough room for nops, an approximate address can be enough to achieve reliable code execution
  - This technique is known as NOP-sled or NOP slide

- Is there anything left in a predictable address?
  - In most cases, yes:
    - Images are usually compiled to run in a fixed known memory address
    - No relocatable shared dynamic libraries
    - Improvement: PIE (Position Independent Execution) code, on Linux platforms (2005)
  - ret2code approaches
- Can we guess the randomly generated addresses?
  - It depends. Low entropy on 32-bits
  - On 32-bit Windows, even lower entropy
- Is there a clever way to find these addresses?
  - Is there a memory leak available?
  - Brute-forcing is always an option

#### Some final remarks

- On Windows, threads of the same application share the memory layout
- On Unix, fork processes replicates the parent memory layout

ASLR is a very strong protection against code execution exploits, but most operating systems do not offer a complete solution

# Defeating control-flow hijacking attacks ASLR on Windows

#### Stack location:

- The time stamp counter (TSC) of the current processor is shifted and masked to a 5-bit value (2<sup>5</sup> options)
- Added to another 9-bit TSC-derived value to make up the base address of the stack

#### **Heap location:**

- TSC shifted and masked to a 5-bit value (2<sup>5</sup> options), multiplied by 64KiB
- The possible heap address ranges from 0x00000000 to 0x001f0000

# Defeating control-flow hijacking attacks **ASLR on Windows**

#### **Executable images location:**

- Load displacement by calculating a  $\delta$  value each time an app runs
- 8-bit pseudo-random number → only one of 256 possible locations
  - TSC shifts four places, and then divides modulo 254 and adds 1
  - The result is then multiplied by the allocation granularity of 64 KiB
- $\blacksquare$  This  $\delta$  value is added to the preferred load address of the image file

# Defeating control-flow hijacking attacks Address Space Layout Randomization (ASLR) in Windows

#### Shared libraries location:

- Load offset is calculated with a system-wide per-boot value called the image bias
  - Stored in a global memory state structure (MI\_SYSTEM\_INFORMATION), in field MiState Sections ImageBias)
- Calculated only once per startup
- Shared memory region between 0x50000000 and 0x78000000
- First DLL is always ntd11. We can calculate its image base address as:
  - 0x78000000 (ImageBias + NtDllSizein64KBChunks)\*0x10000 (32-bit)
  - 0x7FFFFFFF0000 (ImageBias64High + NtDllSizein64KBChunks)\*0x10000 (64-bit)

# Other techniques of defense

#### **Probabilistic methods**

- Instruction Set Randomization
- Data Space Randomization: randomizes the representation of data stored in memory (not location). Encrypts all variables, not just pointers, and using different keys

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#### **Generic methods**

- Data Integrity: spatial memory integrity (protect against invalid memory writes)
- Data Flow Integrity: checks read instructions to detect data corruption before use

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#### Generic methods

- Data Integrity: spatial memory integrity (protect against invalid memory writes)
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## Other defenses against hijacking the flow of control

- Code Pointer Integrity
- Control Flow Integrity (CFI)



# **Exploiting Software Vulnerabilities**

# Software Vulnerabilities Control-Flow Hijacking

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