PhD POSITION at University of Zaragoza

AYUDA PARA LA FORMACIÓN DE PERSONAL INVESTIGADOR (FPI)

GRANT DESCRIPTION / DESCRIPCIÓN DE LA BECA

PROJECT / PROYECTO: Interconnection and Memory in Scalable Computers (TIN201021291C0201)
Principal Researcher / Investigador Principal: Victor Viñals Yúfera
Organismo: Universidad de Zaragoza
Research Group / Grupo de Investigación: Grupo de Arquitectura de Computadores de Zaragoza
Convocatoria: BOE número 31 (5 de febrero de 2011)

Dead line February 21st, 2011 Fecha límite 21 de febrero de 2011

Project Summary:
This Project is mainly focused on parallelism exploitation in high performance computers zooming in on two main components: interconnection networks and memory hierarchy. The cases under study cover from onchip multiprocessors to the largest scale supercomputers. Nowadays, the computer industry is exploring the generalization of parallelism to deploy systems of almost any budget and scale. Due to several constraints, like design complexity and power dissipation, the scalability of traditional microarchitectures is no longer tied to the increase in frequency and ILP. Because of this, the main manufacturers are improving performance by employing numerous cores and memories per chip interconnected with complex onchip networks. Although our two research groups have been primarily dealing with memory microarchitecture, interconnection networks, parallel architectures and applications for years, these topics have never been as important as now. The scalable parallel paradigm is a mainstream and relevant topic, and this makes our work even more praiseworthy nowadays. As experts in these research areas, our opening hypothesis is that many industrial or research solutions for computing scalability are susceptible to improvement. The continuous technological advances and the juvenile behavior of this area of knowledge, make it feasible to provide new solutions with relevant impact in the field. The present project pursues to make contributions of this kind.

More info / Más información:
http://www.micinn.es/portal/site/MICINN/menuitem.dbc68b34d11ccbd5d52ffebeb801432ea0/?vgnextoid=07ffe142b621d210VgnVCM1000001d04140aRCRD&vgnextchannel=8da5b9746e16210VgnVCM1000001034e20aRCRD

REQUIREMENTS / REQUISITOS
European degree or equivalent has to be finished after January 1st, 2007.

- Posterior al 1 de enero de 2007, en el caso de licenciados, ingenieros, arquitectos y graduados o equivalente en sistemas universitarios extranjeros. En el caso de estudios no finalizados (licenciados, arquitectos, ingenieros o graduados) el último requisito o asignatura que le haya dado acceso al posgrado deberá ser posterior al 1 de enero de 2007.
- Posterior al 1 de enero de 2006, en el caso de diplomados, ingenieros técnicos y arquitectos técnicos o equivalente en sistemas universitarios extranjeros. "Capacidad de trabajo en grupo".

If you are interested, please contact:
Si te interesa contacta con:

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