Interesados enviar CV y calificaciones académicas a Roger Espasa, director de investigación en microarquitectura en Intel Labs Barcelona (roger.espasa at intel.com). Este trabajo de investigación está relacionado con diseño y simulación de microarquitecturas orientadas a prestaciones (a post Knight's Corner many integrated core).

Description
This team member will be part of a team responsible for:

- Architectural timing simulator development
- Performance correlation and benchmark/workload analysis/characterization
- Interacting with members of the design team and software team to identify, resolve and analyze architectural performance issues
- Develop performance oriented directed tests
- Develop performance analysis tools

Qualifications
You must possess the minimum qualifications to be initially considered for this position. Experience would be obtained through your educational level research and/or relevant job/internship experiences.

Minimum Requirements:
Masters degree in Computer Science or Electrical Engineering.

- Strong background in microprocessor architecture
- Strong C/C++ programming experience

Valuable Merits:
PhD in Computer Science or Electrical Engineering

- Direct Simulator/Emulator experience
- RTL/Logic design
- 3D graphics algorithms
- Parallel programming
Oferta de Trabajo en Intel Labs Barcelona

Job Category: Engineering
Primary Location: Barcelona, Spain
Full/Part Time: Full Time
Job Type: Recent College Graduate
Regular/Temporary: Contractor Business Group

Employees in the Intel Architecture Group (IAG) deliver innovative platforms across computing and communication segments including data centers, mobile and desktop personal computers, handhelds, embedded devices and consumer electronics. Intel's industry leading technology is used to create integrated hardware and software solutions such as processors, chipsets, communication radios, graphics processors, motherboards, and networking components that deliver capabilities from security and manageability to computing performance and energy efficiency. IAG employees are at the forefront of enabling a new era of computing that is more integrated into all aspects of our daily lives.

Intel Labs Barcelona – Barcelona, Spain
Intel Labs Barcelona (ILBA) was established in February 2002. ILBA conducts research in the areas of processor microarchitecture and software development tools (compilers) for future microprocessors. The Center also has a strong collaboration with the Universitat Politècnica de Catalunya, and is involved in supervising Ph.D. students and teaching a number of courses. The current research focuses on increasing the performance and reliability of future processors while reducing their energy consumption and improving their heat dissipation. A special emphasis is put on Tera-scale processors that consist of many cores with the capability to run many tasks simultaneously.

- Compiler design
- GPU architecture